

# Efficient flexible polymer light emitting diodes with conducting polymer anodes†

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We report polymer light emitting diodes fabricated on flexible poly(ethyleneterephthalate) substrates coated with a layer of poly(3,4-ethylene-dioxythiophene) : poly(styrenesulfonate) that was lithographically patterned to define the anode structure. A blend of poly(9,9-dioctylfluorene-co-benzothiadiazole) and poly(9,9-dioctylfluorene-co-*N*-(4-butylphenyl)diphenylamine) was then spin-coated on top as the emissive layer and the device was completed by vacuum deposition of a LiF/Al bilayer cathode. The resulting yellow light emitting diodes had typical peak power and current efficiencies of 13.7 lm W<sup>-1</sup> and 8.8 cd A<sup>-1</sup> respectively, which compare well with values for similar devices fabricated on ITO-coated rigid glass substrates. A maximum luminance in excess of 7300 cd m<sup>-2</sup> was achieved.

Colour displays based on organic light-emitting diodes (OLEDs) are being actively developed as an alternative to liquid crystal displays. The first generation of OLED displays are supported on rigid glass substrates, but there is considerable interest in using flexible plastic substrates (that for instance permit the use of roll-to-roll manufacturing techniques) to fabricate robust and conformable displays. OLEDs have, for example, been fabricated on poly(ethyleneterephthalate) (PET),<sup>1–7</sup> polycarbonate,<sup>8</sup> a poly(bis-cyclopentadiene) condensate,<sup>9</sup> poly(propylene adipate)<sup>10</sup> and polyimide.<sup>11,12</sup> The adoption of such materials presents a number of technological challenges including how to cope with their relatively poor thermal stability, a limitation that constrains their use to low-temperature processing routes. This has immediate implications for device fabrication since indium tin oxide (ITO; the dominant anode material for OLEDs) is typically deposited by thermal evaporation or sputtering at temperatures in excess of 350 °C, and even when deposited at lower temperature generally needs to be annealed in air or oxygen at those temperatures in order to achieve a suitably high conductivity. Methods of deposition on plastic substrates at lower temperatures are under development but to date they typically yield somewhat porous films with relatively low conductivity, limited optical transmission and degraded substrate adhesion, and lead ultimately to reduced device quality.<sup>13</sup> The use of a biased RF magnetron sputtering process may hold promise in this context.<sup>10</sup>

These issues, together with the tendency of ITO to crack when the substrate is flexed, have led researchers to seek alternative anode materials for flexible substrate applications. A variety of materials systems have been proposed including

other metal oxides,<sup>14</sup> thin metallic films,<sup>15</sup> polymer–metal composites,<sup>13</sup> polymer–fullerene composites,<sup>16</sup> and conducting polymers.<sup>1,17,18</sup> Polymer based approaches are especially appealing because they allow deposition over large areas using techniques such as blade-coating, and planographic or inkjet printing, with obvious potential for reel-to-reel implementation. The two most widely used conducting polymers in this respect are polyaniline,<sup>1</sup> and poly(3,4-ethylenedioxythiophene) : poly(styrenesulfonate) (PEDOT : PSS).<sup>17</sup> Of these, PEDOT : PSS is the more common choice for display applications because polyaniline typically has a somewhat reduced transmission across the visible spectrum, with an absorption peak at 440 nm that prevents its use in full colour displays. PEDOT : PSS, on the other hand, has a high transparency across the whole visible spectral range which, together with its excellent hole-injecting properties,<sup>19</sup> has led to it being successfully utilised in a wide variety of OLEDs. It was, consequently, the material that we selected to use as the anode in our flexible substrate devices.

Our PET substrates were commercially sourced overhead projector sheets and the anode patterns were lithographically defined in 120 nm spin-coated layers of standard Baytron P<sup>20</sup>—a relatively high conductivity formulation of PEDOT : PSS from H. C. Starck GmbH. The Baytron P coated substrates were then spin-coated with an additional 40 nm layer of Baytron P VP AI4083—a lower conductivity formulation of PEDOT : PSS also supplied by H. C. Starck GmbH. This second layer is able to form a good contact with the active light-emitting polymer layer and we have generally found that its inclusion yields devices of substantially higher quality than those using the directly patterned Baytron P alone.<sup>21</sup> PET substrates with these two anode layers on top were next annealed at 120 °C for 20 minutes in a dry nitrogen atmosphere to remove trapped water, the presence of which can reduce the PEDOT : PSS conductivity.<sup>22</sup> Next, 90 nm emission layers, here comprising a 1 : 1 blend by weight of poly(9,9-dioctylfluorene-co-benzothiadiazole) (F8BT) and poly(9,9-dioctylfluorene-co-*N*-(4-butylphenyl)diphenylamine)

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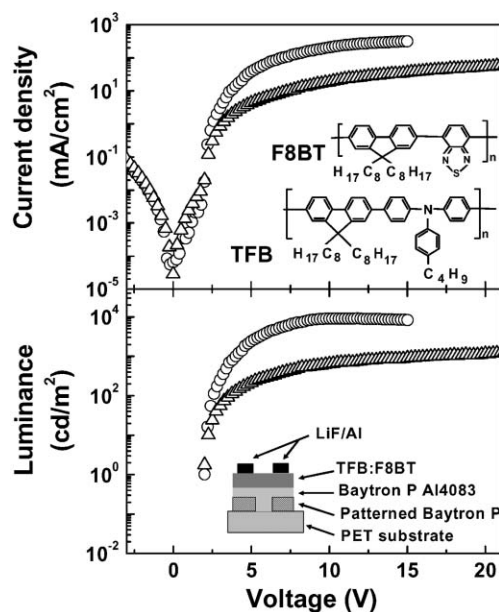
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† The HTML version of this article has been enhanced with colour images.

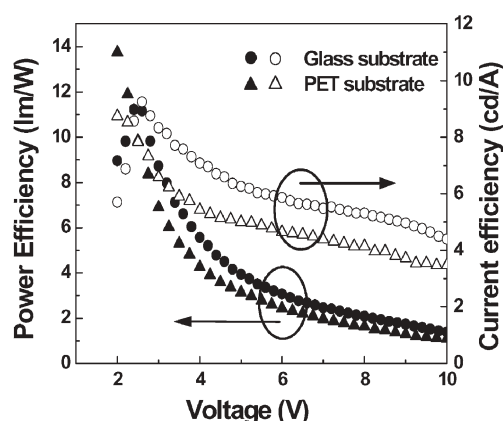
(TFB), were spin coated from 20 mg ml<sup>-1</sup> xylene solution on top of the PEDOT : PSS. The resulting films were dried at 60 °C for two hours in a dry nitrogen atmosphere and finally a top contact comprising 1 nm of LiF capped with 100 nm of Al was thermally deposited through a shadow mask onto the polymer blend films *in vacuo* (2 × 10<sup>6</sup> Torr). The pixel area, defined by the overlap of the anode and cathode, was ~1.5 mm<sup>2</sup>. The schematic structure of our flexible LEDs is shown in the inset to Fig. 1. For comparison, devices of similar structure were fabricated using patterned ITO-coated glass in place of the Baytron P coated PET substrates. The remaining layers were the same as for the PET devices, namely: Baytron P VP Al4083 (60 nm)<sup>23</sup>/TFB : F8BT 1 : 1 blend (90 nm)/LiF (1 nm)/Al (100 nm). Electroluminescence (EL) spectra for the two device types were measured with a CCD spectrometer (Ocean Optics USB2000). The corresponding current–voltage characteristics were measured using a Keithley 2410 Source Measure Unit and the luminance was measured with a calibrated TOPCON Luminance Meter.

Fig. 1 shows the current density *vs.* voltage and luminance *vs.* voltage (*J–V–L*) characteristics for both the rigid glass and flexible PET substrate devices. The two devices have similar turn-on voltages of ~1.9 V but the current density and luminance of the PET device are significantly lower than those of the glass substrate device, *e.g.* at 5 V  $J_{\text{PET}}/J_{\text{glass}} \approx L_{\text{PET}}/L_{\text{glass}} \approx 10\%$ , due to the higher conductivity of ITO compared to Baytron P, namely ~2000 *versus* ~5 S cm<sup>-1</sup>.

The efficiency data for the two devices are shown in Fig. 2. Peak efficiencies of 13.7 lm W<sup>-1</sup> and 8.8 cd A<sup>-1</sup> were obtained

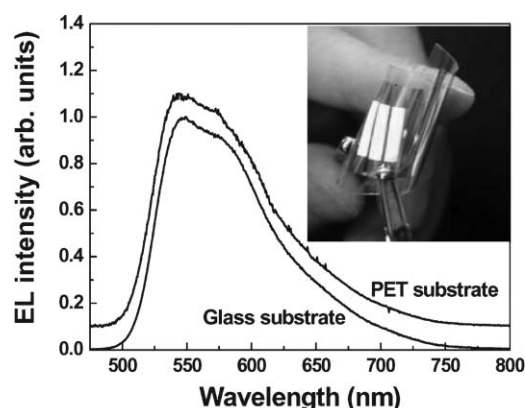


**Fig. 1** Current density *vs.* voltage (upper panel) and luminance *vs.* voltage (lower panel) characteristics for rigid-substrate devices fabricated on ITO-coated glass (open circles) and flexible-substrate devices fabricated on Baytron P coated PET (open triangles). The device structures were [ITO/Baytron P Al4083/TFB : F8BT/LiF : Al] and [Baytron P/Baytron P Al4083/TFB : F8BT/LiF : Al] respectively. The upper panel insets show the F8BT and TFB chemical structures. The inset in the lower panel is a schematic of the PET-substrate device architecture.



**Fig. 2** The variation with applied voltage of the lm W<sup>-1</sup> (filled symbols) and cd A<sup>-1</sup> (open symbols) device efficiencies for the two devices shown in Fig. 1. Circles are for glass substrate devices and triangles for PET substrate devices.

for the PET substrate device, with efficiencies of 5.3 lm W<sup>-1</sup> and 5.9 cd A<sup>-1</sup> at 100 cd m<sup>-2</sup>. These values compare with peak efficiencies of 11.2 lm W<sup>-1</sup> and 9.2 cd A<sup>-1</sup> for the glass substrate device, which were obtained at a brightness of 100 cd m<sup>-2</sup>. The efficiencies of our PET devices compare very favourably with those reported for other flexible substrate devices.<sup>18,24</sup> For example, using *meso*-erythritol (1,2,3,4-tetrahydroxybutane) as an additive, Ouyang *et al.*<sup>18</sup> prepared PEDOT : PSS anodes with a conductivity 150 times higher than that of standard Baytron P. The efficiency of the resultant EL devices was 1.2 cd A<sup>-1</sup> at 200 cd m<sup>-2</sup>. We find 5.1 cd A<sup>-1</sup> for our PET devices at the same luminance. Our results also compare reasonably well with current state of the art glass substrate devices. Importantly, the cd A<sup>-1</sup> efficiency of our flexible substrate devices remains high (>4 cd A<sup>-1</sup>) up to 8 V, at which voltage the luminance reaches ~500 cd m<sup>-2</sup>. The maximum luminance attainable with our flexible device was 7300 cd m<sup>-2</sup>, although this required a high drive voltage of 80 V for reasons that are discussed below.

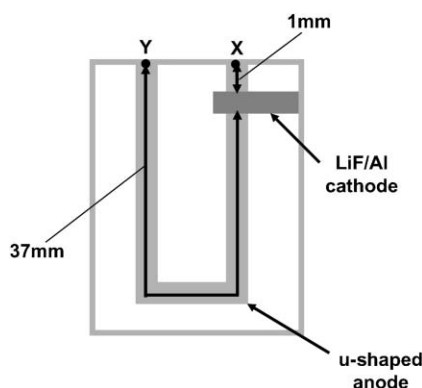


**Fig. 3** Electroluminescence spectra for the two devices shown in Fig. 1. The PET substrate device spectrum (upper curve) has been vertically displaced for clarity. The CIE 1931 (*x*, *y*) coordinates for both devices are (0.455, 0.535). The inset shows a photograph of a flexed PET substrate device operating under ambient conditions (see the HTML version of this article for a colour version of this image).

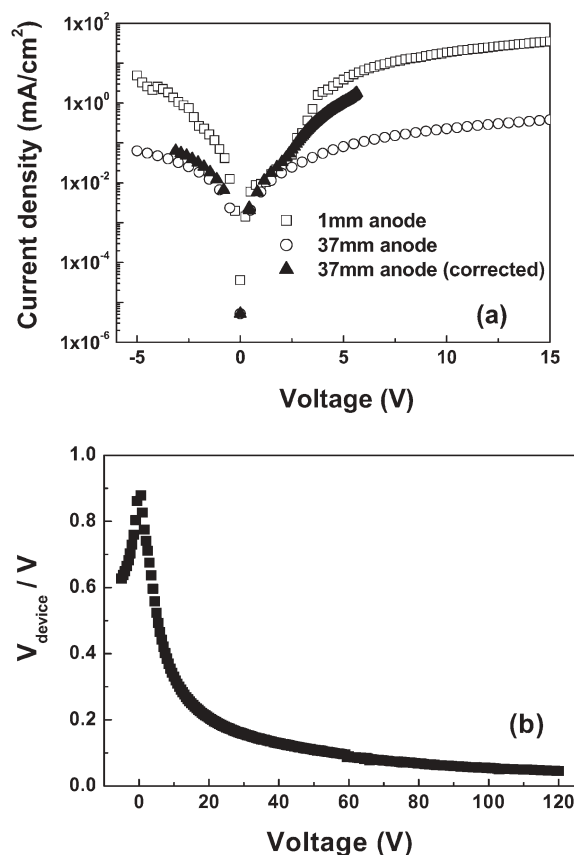
Fig. 3 shows the electroluminescence spectra for the PET and glass substrate devices. No appreciable difference is apparent and the spectra are displaced vertically since otherwise they closely overlap. The inset to Fig. 3 shows an unencapsulated flexible substrate device with pixel size 2 mm  $\times$  8 mm working under ambient conditions. The device emits bright yellow light with CIE 1931 ( $x$ ,  $y$ ) coordinates (0.455, 0.535), and continues to function without any obvious problems when rolled up tightly (radius,  $r_B < 4$  mm). This compares favourably with reports for ITO-coated flexible substrates, for which the anode resistance is found to increase sharply when  $r_B < 8$  mm.<sup>25</sup>

As noted above, the higher resistance of the flexible device is directly attributable to the lower conductivity of PEDOT : PSS compared with ITO. The total device resistance,  $R$ , may be approximated as the sum of an axial (normal-to-plane) device resistance,  $R_{\text{axial}}$ , and an in-plane anode sheet resistance,  $R_{\text{in-plane}}$ . This latter resistance is relatively high for the flexible PET device.  $R_{\text{axial}}$  dominates below the turn-on voltage, even though the axial dimension of  $\sim 100$  nm is very much smaller than the in-plane dimension of  $\sim 2$  mm. Above turn-on, however, the rate of carrier injection into the active layer increases rapidly, leading to a sharp drop in  $R_{\text{axial}}$  and a total resistance that is more and more dominated by  $R_{\text{in-plane}}$ . A steadily increasing fraction of the applied bias is consequently dropped across the anode.

To investigate the influence of the sheet resistance on the device performance, an additional flexible device was fabricated in which the Baytron P anode was patterned in a u-shape as depicted in the plan-view schematic of Fig. 4. Contact was made to the PEDOT : PSS at two different locations, X and Y. In the first configuration (X), the in-plane distance to the region directly beneath the cathode was 1 mm. In the second configuration (Y), the in-plane distance was 37 mm and the influence of the sheet-resistance on the current–voltage characteristics was consequently stronger. The  $J$ – $V$  characteristics for these two situations are shown in Fig. 5(a) (open symbols: squares for short anode length and circles for long)



**Fig. 4** Schematic showing an LED fabricated with a u-shaped PEDOT : PSS anode. Contact can be made to the anode at two different positions X and Y, resulting in respective in-plane distances of 1 and 37 mm from the point of contact to the region directly beneath the cathode. In the latter configuration, the current–voltage characteristics are more strongly influenced by the relatively high sheet resistance of the PEDOT : PSS anode.



**Fig. 5** (a) Current density ( $J$ ) vs. voltage ( $V$ ) characteristics for the device configurations illustrated in Fig. 4. The anode was in turn contacted at points X (1 mm anode length; open squares) and Y (37 mm anode length; open circles). The influence of the additional anode resistance when contacted at Y is apparent and results in a greatly reduced current density. The solid triangles data represent the  $J$ – $V$  characteristics for the 37 mm anode length device after correction for the estimated voltage drop across the in-plane PEDOT : PSS sheet resistance (see text for details). (b) The calculated (see text for details) fractional voltage drop,  $V_{\text{device}}/V$ , across the emissive layer for the 37 mm anode length device as a function of applied voltage,  $V$ .

and the effect of the increased sheet resistance is obvious. An upper limit for the sheet resistance in the elongated-anode device configuration ( $R_{\text{in-plane}} = 3.09 \text{ M}\Omega$ ) can be estimated from the measured resistance at 120 V (for which the majority of the potential is dropped across  $R_{\text{in-plane}}$ ). This resistance may in turn be used to deduce a lower limit for the potential,  $V_{\text{device}}$ , dropped across the device at lower biases:  $V_{\text{device}} > V_{\text{applied}} - I \times R_{\text{in-plane}}$ . The calculated lower limit for the fractional voltage drop,  $V_{\text{device}}/V_{\text{applied}}$ , across the elongated-anode device is shown in Fig. 5(b). For biases below 5 volts, over 50% of the applied voltage drops across the diode, whilst at 10 V only  $\sim 30\%$  of the applied bias is dropped across the device. In this regime, the in-plane resistance of the anode presents a severe limitation for device power efficiency. Using the effective device voltages derived above, it is possible to correct the current–voltage characteristic for the elongated device. The resulting data (filled triangles) are also plotted in Fig. 5(a) and agree fairly well with the (uncorrected) data for the short-anode device configuration, indicating that the

decomposition of the total device resistance into additive in-plane and axial contributions is a reasonable first approximation. In passing, we note that the effects of sheet resistance can also be observed for ITO anodes, albeit at much higher currents due to the higher conductivity of ITO.<sup>26</sup>

In summary, we have reported the characteristics of flexible PET-substrate-based polymer LEDs with anodes comprising the conducting polymer PEDOT : PSS, without an underlying ITO layer. These devices with fluorene-based polymer blend emission layers give efficiencies of 5.3 lm W<sup>-1</sup> and 5.9 cd A<sup>-1</sup> at a luminance of 100 cd m<sup>-2</sup>, values that compare favourably to other flexible devices reported in the literature and indeed to similar devices on ITO-coated glass substrates. The sheet resistance of the standard Baytron P formulation (as used here) is, however, rather too high and leads to a requirement for undesirably large drive voltages. Conversely it is well known that the conductivity of PEDOT : PSS films can be substantially enhanced by incorporating polyalcohols or high dielectric solvents into the PEDOT : PSS dispersion prior to deposition.<sup>18,22,24</sup> We therefore anticipate that improvements in device power efficiency should be achievable by further optimisation of the polymer anode properties. This is the focus of ongoing studies.

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